RESPONSE

The Examiner required restriction to one of the following patentably distinct inventions under 35 U.S.C. 121:

    I.   Claims 1-17 and 29-36, drawn to a method/system to access memory by traversing a queue of commands containing a Next Valid Write Address pointer; or

    II.   Claims 18-28, drawn to a processor for issuing commands in a queue.

The Applicant respectfully traverses the requirement for restriction made by the Examiner under 35 U.S.C. 121 on the grounds that examination of all claims would not impose an undue burden on the examiner.

Notwithstanding the foregoing, the Applicant provisionally elects the claims of Group I, claims 1-17 and 29-36, drawn to a method/system to access memory by traversing a queue of commands containing a Next Valid Write Address pointer.

If it is felt for any reason that direct communication with the Applicant's attorney would serve to advance prosecution of this case to finality, the Examiner is invited to call the undersigned Peter P. Scott at the telephone number below.

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AUTHORIZATION TO PAY AND PETITION
FOR THE ACCEPTANCE OF ANY NECESSARY FEES

If any charges or fees must be paid in connection with this communication (including but not limited to the payment of an extension fee or issue fees), or if any overpayment is to be refunded in connection with the present application, any such charges or fee, or any such overpayment may be respectively paid out of, or into, the Deposit Account No. 12-2252 of LSI Logic Corporation. If any such payment also requires a Petition or Extension Request, please construe this authorization as the required Petition or Request.

Respectfully submitted,

Richard L. Solomon

By: _____

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Date: ___9/13/05___

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